


<b>Search Notes</b>  	<b>Application/Control No.</b>  09989777	<b>Applicant(s)/Patent Under Reexamination</b>  NEMECEK, CRAIG
	<b>Examiner</b>  Jacob, Mary C	<b>Art Unit</b>  2123

### SEARCHED

Class	Subclass	Date	Examiner
703	23,26	4/14/10	MCJ
714	28	4/14/10	MCJ

### SEARCH NOTES

Search Notes	Date	Examiner
Updated Inventor Name Search: EAST; Google Scholar	4/14/10	MCJ
Assignee Name Search: EAST; Google Scholar	4/14/10	MCJ
Updated East Search: see search history	4/14/10	MCJ
Updated Google Scholar Search: cypress semiconductor and sleep or stall, clocks; emulation sleep stall clocks "device under test"; emulation sleep clocks "device under test" lock step; emulation stall clocks "device under test" lockstep	4/14/10	MCJ
Updated IEEE Search: lockstep or lock step, sleep or stall, clock or clocks, device under test or DUT	4/14/10	MCJ

### INTERFERENCE SEARCH

Class	Subclass	Date	Examiner
	Updated PGPUB/UPAD text search-see EAST search history printout	4/14/10	MCJ

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